AcceleratorApplianceDrive–KIWIV2.0system

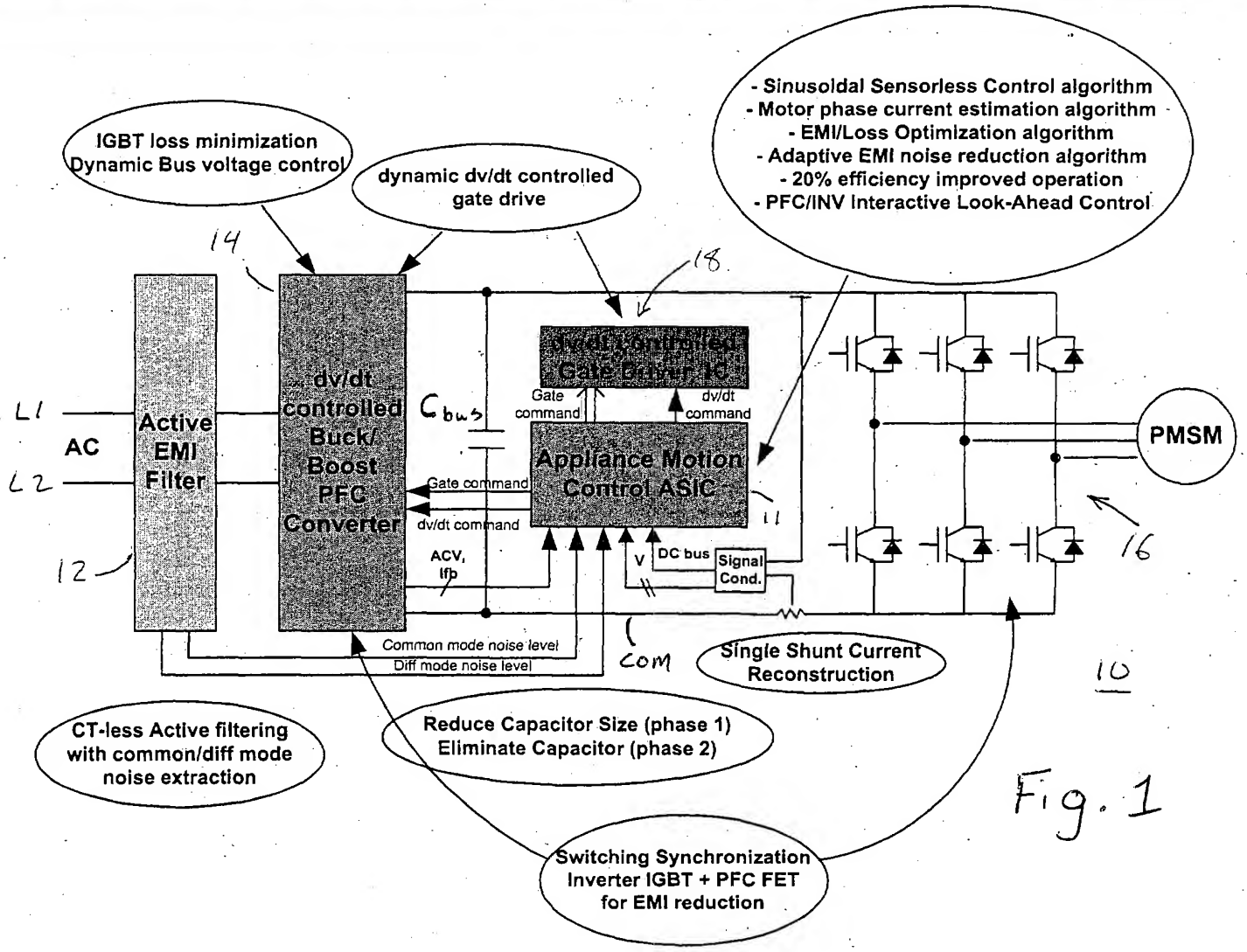
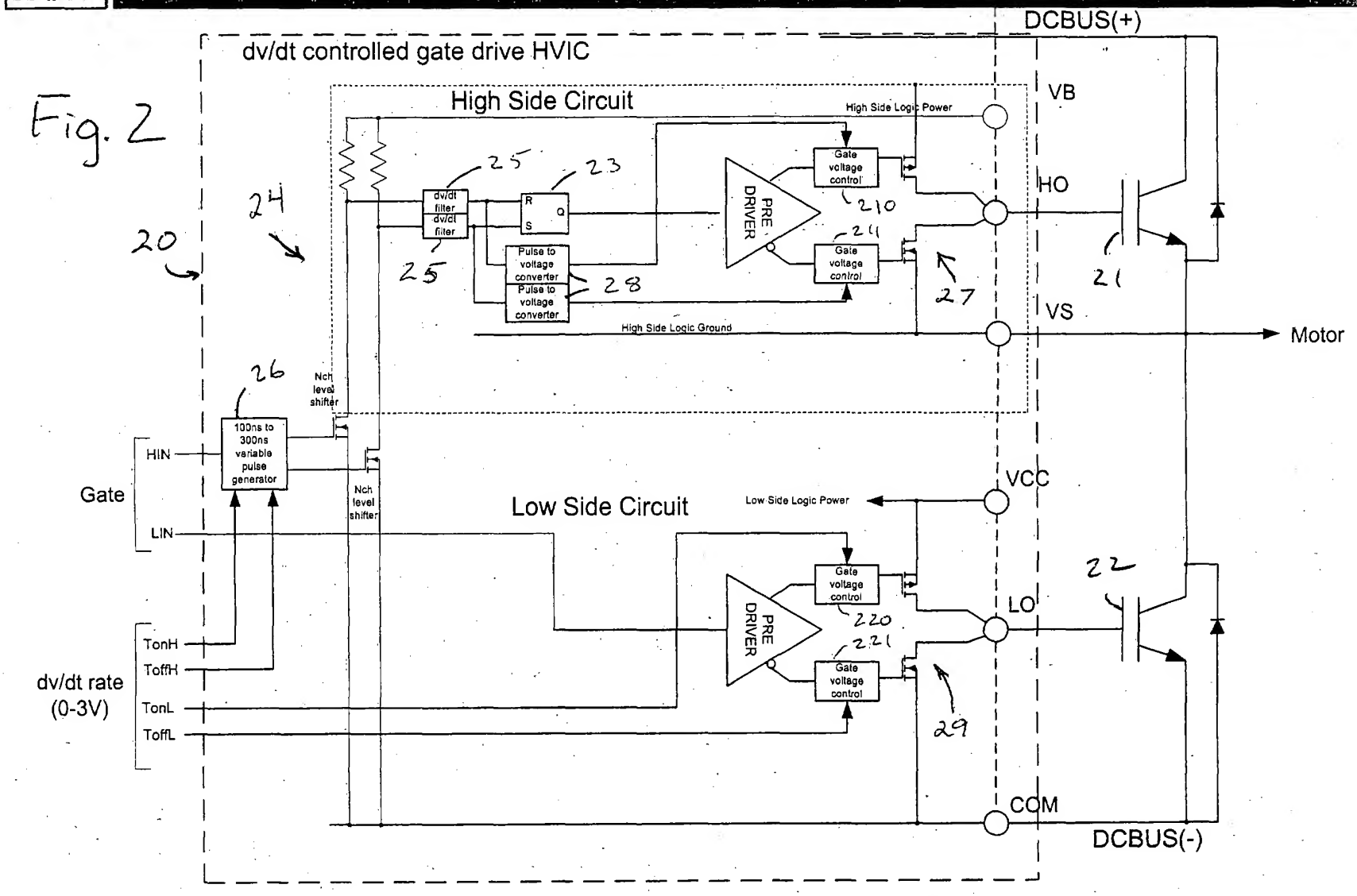


Fig. 1

GlobalClosedloopEMIcontrol– dv/dtcontrolledHVIC



GlobalClosedloopEMIcontrol– ControlMethod

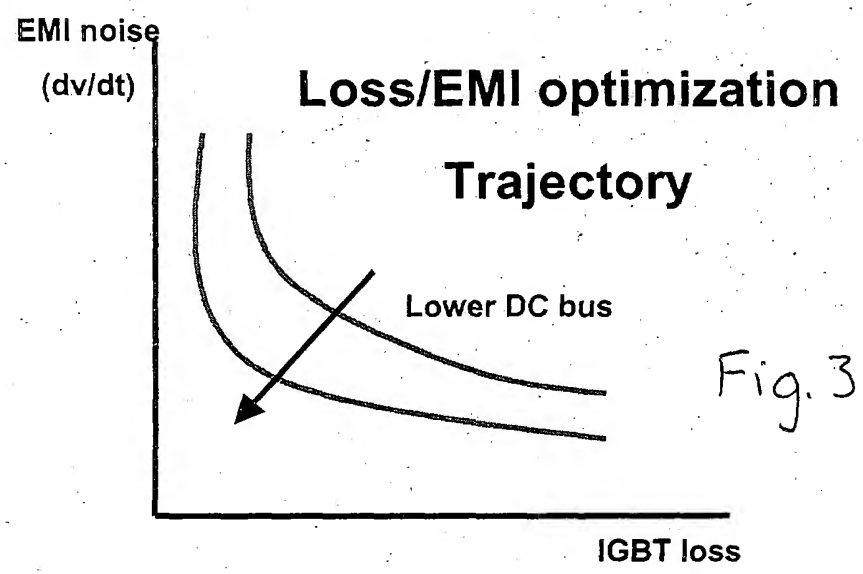
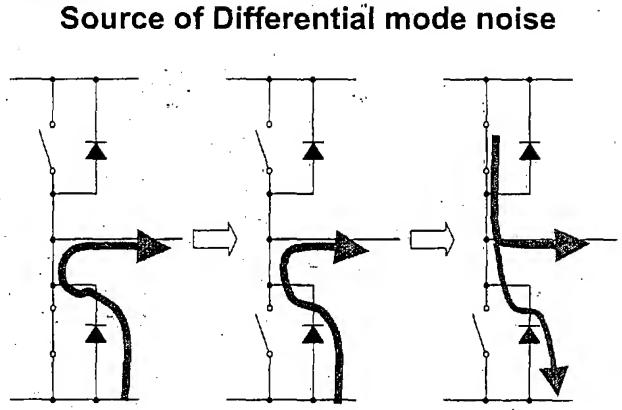


Fig. 3



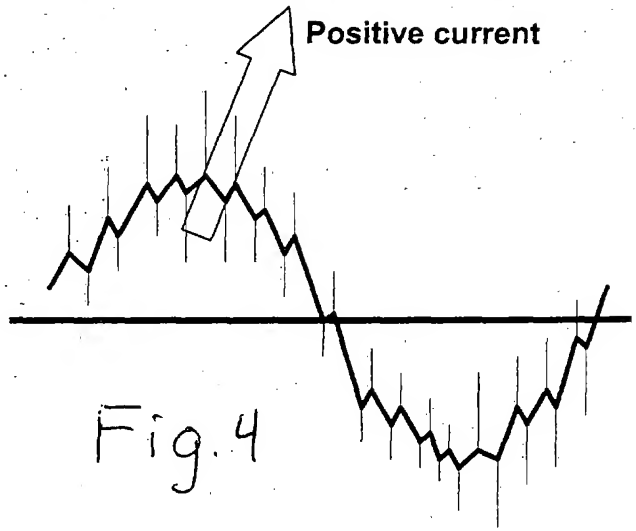


Fig. 4

Adaptive Control

Control walks through the loss/EMI minimization trajectory

For differential mode noise reduction:

Slow dv/dt for only Ton

For common mode noise reduction:

Slow dv/dt for Ton and Toff

GlobalClosedloopEMIcontrol:CT-LessActiveFilter

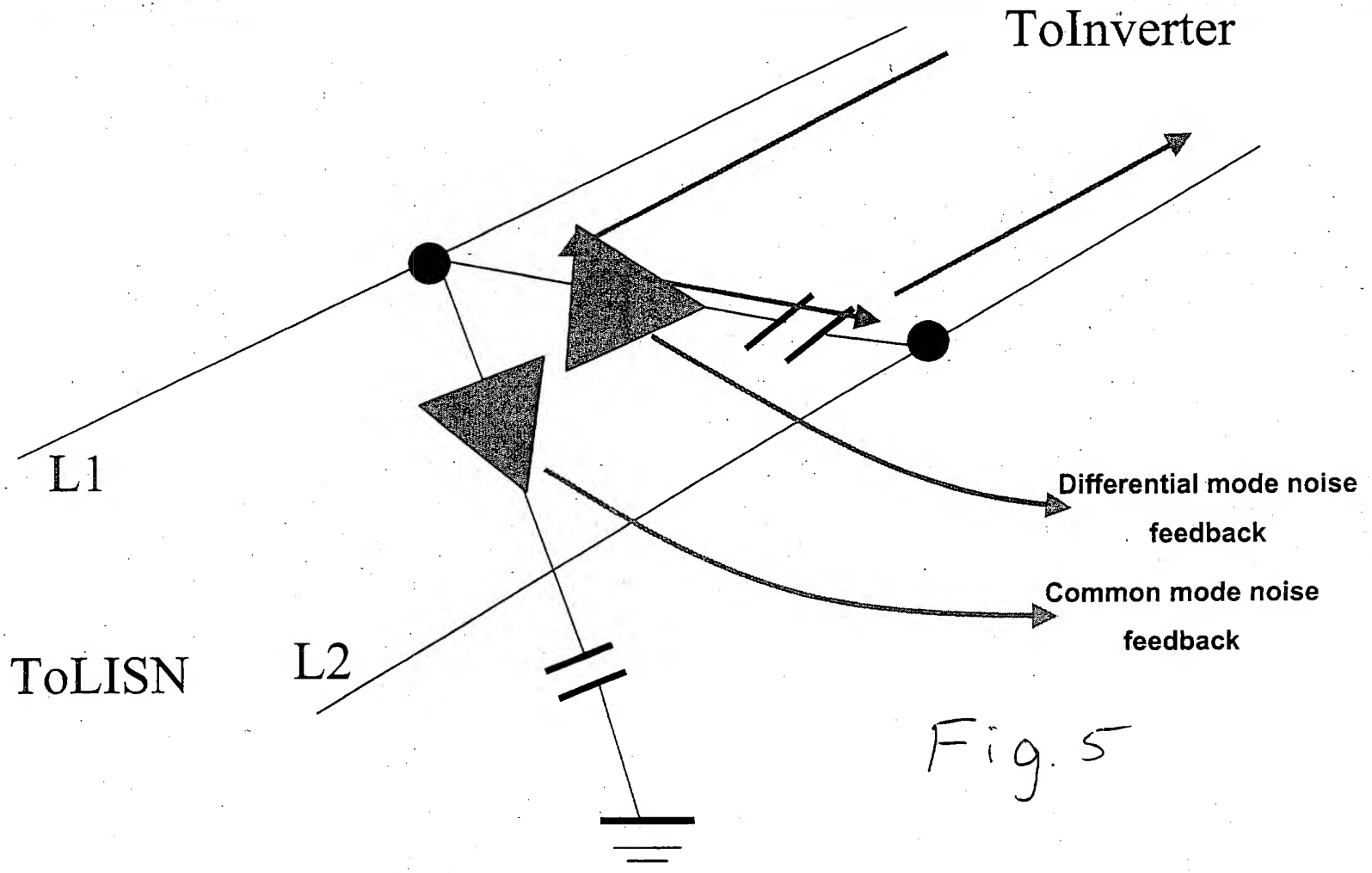


Fig. 5

GlobalClosedloopEMIcontrol:Fastnoisesampling

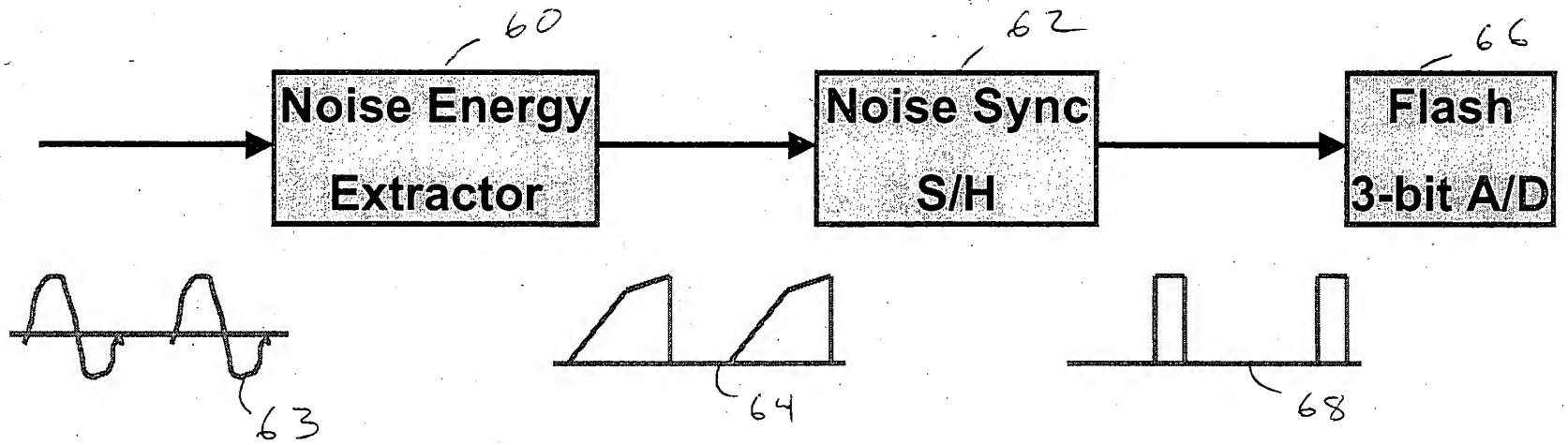


Fig. 6

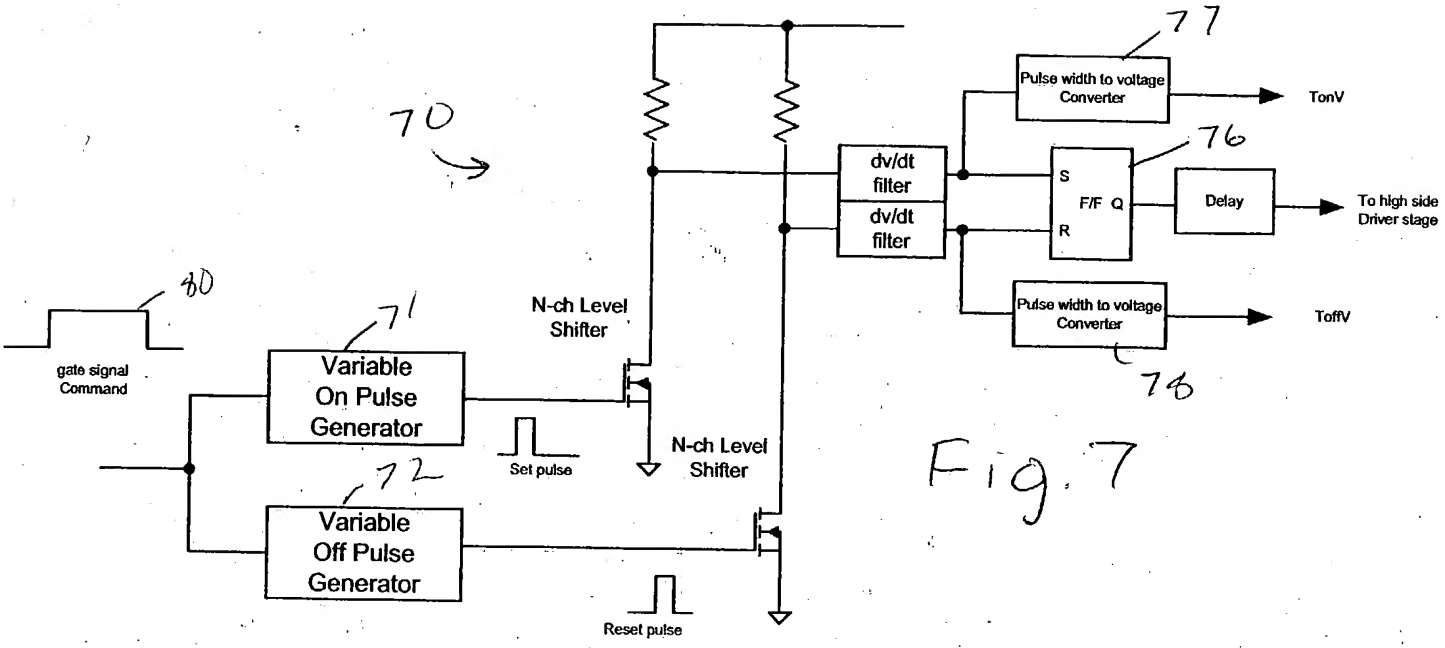


Fig. 7

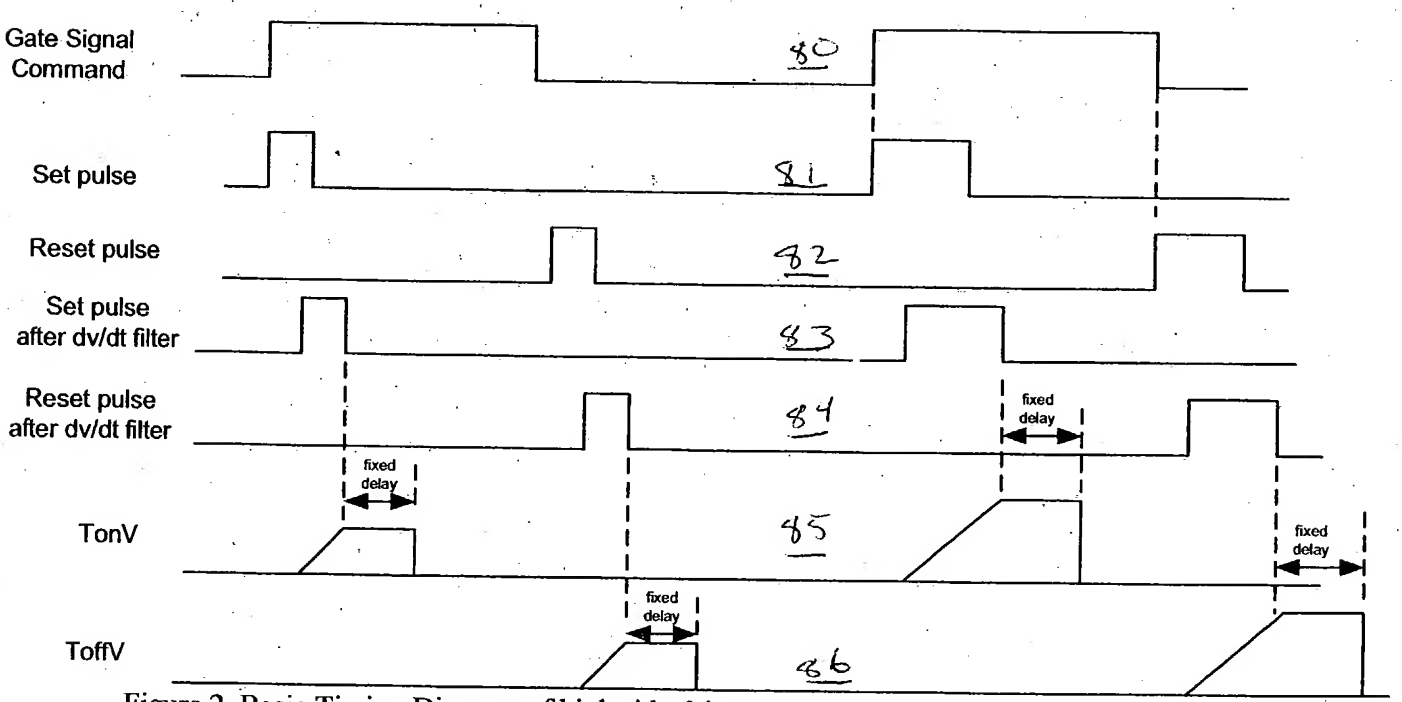
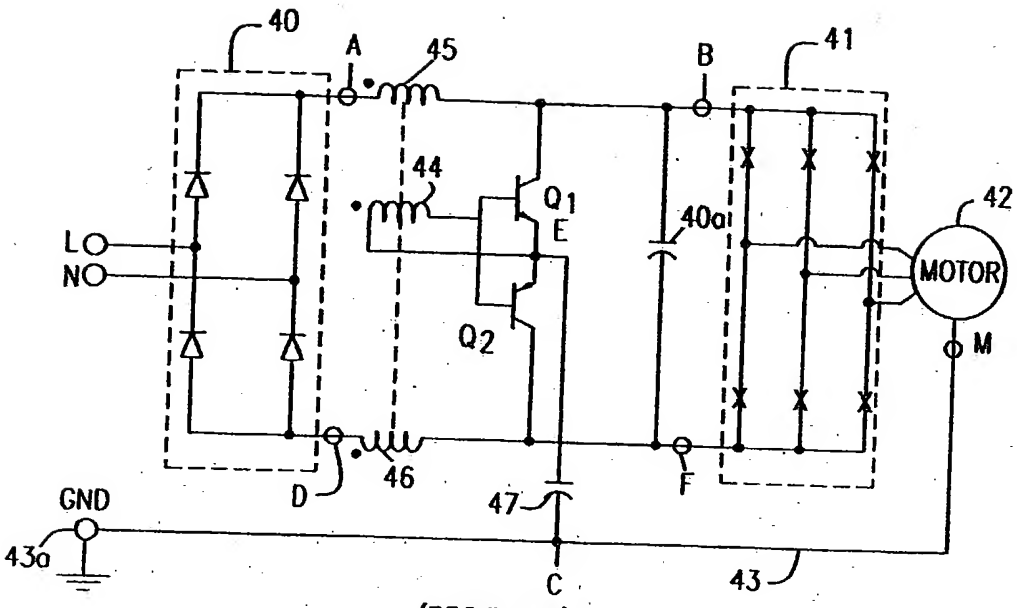


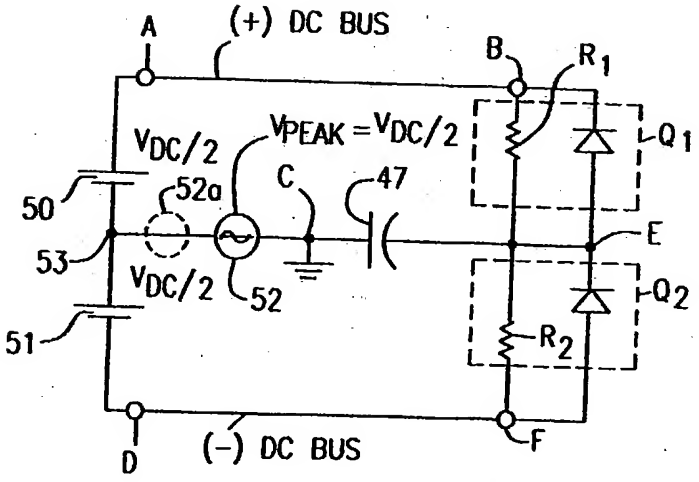
Figure 2 Basic Timing Diagram of high side drive

Fig. 8



(PROIR ART)

FIG. 9



(PROIR ART)

FIG. 10